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# A Pattern Recognition Approach for Differential Protection Method to Detect Transformer Faults

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**Abstract**: The differential relay is used for detecting the faults within the transformer. But, a conventional differential relay has many demerits. In recent times, the false counter of differential relay has been reported where it was not at all required. This brings urge among researchers for the development of advance classifier algorithm which clearly states about the faults and disturbances and helps to avoid the false tripping at times of disturbances. In this paper, Authors have used the approach based on pattern reorganization of all possible operating conditions of transformer and using support vector machine (SVM), the classification of faults and disturbances has been carried out. The proposed scheme gives good results as compared with conventional differential protection method and overall efficiency of the system found out to be 92.89%

**Keywords:** Discrete Wavelet Transform, Daubechies Wavelet, Differential Protection, MATLAB Simulink, Support Vector Machine.

## I. INTRODUCTION

In transformer, differential protection is vastly employed to detect faults within transformer with high degree of accuracy and should be able obsolete the disturbances where tripping is not required [1], [2]. But, during switching inrush condition, recovery inrush condition, sympathetic inrush condition, over-fluxing condition etc., the over-current relay in differential circuit operates unnecessarily.

In earlier days, the harmonic restrain method is very useful for detecting the conditions like charging condition or overfluxing condition in percentage bias differential relay scheme. But with advent in improved core of transformer and due to parallel distributed capacitances of Extra High Voltage (EHV) transmission lines, these relay reports its maloperation [3], [4].

Since the magnetizing and fault current are non-stationary and non-periodic fast electro-magnetic transients, for processing the signals, discrete wavelet transform (DWT) is used. DWT enables the study of transients without the loss of identity. In this paper, the physical model of two winding, 2 kVA, 50 Hz, 230 V/ 230 V single phase transformer is simulated in MATLAB Simulink platform for the study of various operating conditions. Obtained results from simulations are processed through DWT to obtain the coefficients at different decomposition levels. These decomposed coefficients are used for training and testing of proposed SVM algorithm.

#### **II. CASE STUDY**

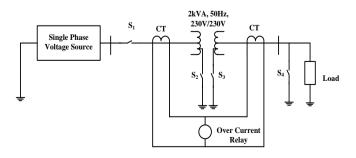


Fig.1: System configuration of simulated differential protection scheme



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Fig. 1 shown above has been simulated in MATLAB Simulink platform using Sim Power System (SPS) toolbox inherently present in MATLAB. It consist of Single Phase Supply from Grid and values of voltage and frequency of source is set equal to 230V, 50 Hz respectively. It has a two winding transformer of 2 kVA, 50 Hz, 230V /230 V ratings. Appendix 1 gives the parameters of two winding transformer. It has two current transformers (CT). In this paper, Authors have used saturable transformer of SPS toolbox to simulate the CT. Since the voltage rating on two sides of transformer is same, two identical CTs of rating 25VA, 10/1 A are used to scale down the currents on two sides of transformer so that the differential current during normal operating condition is near to zero. Many of the previous researchers have used constant block to simulate current transformer [5]-[8]. The parallel RLC load block is used to implement a linear load whose voltage and frequency is set equal to 230 V and 50 Hz respectively. The real and reactive power can be specified for load. Switch S<sub>1</sub> is used to simulate the energization operation of transformer at the time of switching. Switches S<sub>2</sub> and S<sub>3</sub> are used to simulate the internal faults (inter-turn faults on winding 1 and 2) i.e. faults inside the CT locations. The percentage of shorted turns can be varied. Switch S<sub>4</sub> is used to simulate external fault i.e. fault outside the CT location.

In this paper, small sized single phase transformer is used for simulation of differential relay model and can also extend its use for high capacity power transformers.

#### III. DISCRETE WAVELET TRANSFORM

Discrete wavelet transform is the decomposition and discretization of Continues wavelet transform. DWT provides ease of study for any kind of signals and can be reconstructed back to original state. Level of decomposition can be varied and it has different types of coefficients during each decomposition level. These coefficients obtained after decomposition process can be analysed using any of the proposed methods in literature. In this paper, Authors have used Mallat's Multi Resolution Analysis [9] for the study of transient signals using DWT. In the process of analysis using Mallat's scheme, the signal is first passed through FIR filter and then through the low pass filter to obtain the approximate coefficients and further it is passed through a high pass filter to get detail coefficients [10]. Generally, any transient signal has high frequency component present in it and hence called transient signal and has maximum weightage. So, the coefficients obtained after high pass filter are called as detail coefficients. As stated earlier, the signal can be reconstructed with Inverse DWT operation using detail and approximate coefficients.

The DWT is given by,

$$\psi_{m,n}(t) = \frac{1}{\sqrt{2^m}} \psi\left(\frac{t - 2^m n}{2^m}\right) \qquad (1)$$

where,

",  $\psi$  is the mother wavelet.

'm' is the scaling parameter.

'n' is the translation index at each decomposition level.

Selection of mother wavelet is very important for study of any signals using DWT. Mother Wavelet can be any signal which is periodic in nature i.e. average mean of signal over one cycle should be zero. There are many readymade choices of mother wavelet available in literatures [11]. In this paper, Authors have chosen db6 as mother wavelet.

#### A. Daubechies Wavelet

Daubechies belongs to the family of orthonormal bases and invented by Ingrid Daubechies. It widely used choice of mother wavelet in DWT. 'db6' has six levels of decomposition [12].

#### **IV. SVM Based Classifier Algorithm**

#### A. Support Vector Machine

The SVM is introduced by Boser, Vapnik & Guyon. It is method of optimization and based on feature extraction in statistical form. In general, SVM tries to learn structure of data used for its training purpose. For example, to map  $X \rightarrow Y$ , where  $x \in X$  and  $y \in Y$  (class of object). So, one can have data like  $(x_1, y_1), (x_2, y_2), \dots, (x_n, y_n)$  which can be used for training the SVM. So, x and y will have some relationship in between them and can be given by,

$y=f(x,\alpha)$	()	2)

where,  $\alpha$  is the parameter of the function



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So, one can try to learn  $f(x, \alpha)$  such that chosen function work on training data without any training error and hyperplane designed by SVM for classification will not have any false recoganization. Vapnik states that the maximum risk can be defined using empirical risk with addition term

$$R(\alpha) \le R_{emp}(\alpha) + \sqrt{\frac{h(log(\frac{2m}{h}+1) - log(\frac{\eta}{4})}{m}}_{(3)}$$

Where,

'h' is the VC dimension set parameterized by  $\alpha$ .

In general, VC dimensions means set of all functions is the maximum number of points separated by all the possible means. In context to above, SVM based algorithm is proposed. Fig. 2 shows the schematic block diagram of proposed algorithm.

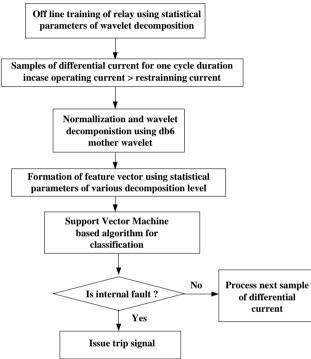


Fig. 2: Schematic block diagram of the proposed scheme

### V. DATA ACQUISITION

For internal faults, different sets of data are generated both for training and testing purpose. For example in internal faults, different cases are observed like inter-turn fault on primary and secondary sides, inter-turn faults during switching operation and inter-turn faults with windings shorted. In case of inter-turn fault on primary side, different percentages of shorting ratio are considered like 16%, 33%, 50%, 66%, 83% and 100%. Similarly, data is collected on secondary side of transformer. Also during inter-turn fault during switching from 0 sec. to 0.02 sec. at step size of 0.002sec. on primary side as well as secondary side of the transformer. Also for inter-turn faults at different loading conditions both at primary and secondary sides. Cumulatively, from all the cases stated above 454 cases are generated. Similarly for non-fault conditions like saturation and over-fluxing conditions, data has been collected of order 222. Overall data generated for training and testing purpose of machine is 676.

#### VI. SIMULATION RESULTS

Out of all the possible operating conditions transformer can encounter, following is the list of all the possible events.

- A. Loaded condition
- B. Internal faults
- C. External faults
- D. Magnetizing inrush condition
- E. Over-fluxing condition



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#### B. Loaded Condition

During loaded condition, the load current should be less the restrain current and relay should not operate. In Fig. 3, relay output is at zero magnitude level which represents the correct operation of relay.

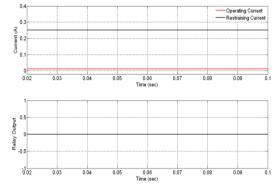


Fig. 3: Relay output for load condition with operating and restrain current.

#### C. Internal Fault

When few turns of the winding are shorted or when line to ground fault take place at the terminal of a transformer within the CT locations, the operating current exceeds the restraining current and relay issues trip signal. Fig.4 shows results with 50% winding on primary side shorted and relay output is at high level which ensures the correct operation relay.

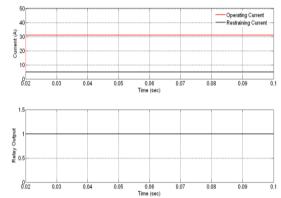


Fig. 4: Relay output for an internal fault with operating and restrain current.

#### D. External Fault

When line to ground fault occurs at load end outside CT location, the relay should not operate. Fig. 5 shows the operating current less than the restraining current and the relay output is low, which ensures the correct operation of relay and provides the through fault stability to system.

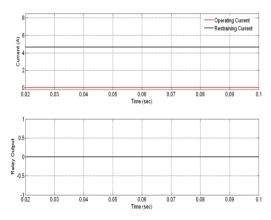


Fig. 5: Relay output for an external fault with operating and restrain current



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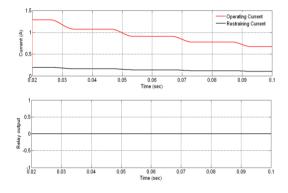


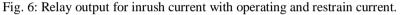
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#### E. Magnetizing Inrush Condition

The magnetizing inrush condition occurs in transformer during its energization because of core saturation. There is also possibility of magnetizing inrush condition when core of transformer is partially saturated. This current can be higher magnitude of order 8 to 10 times that of full load current. In this case, due to flux building in core, heavy current will be drawn and operating current will be greater than restrain current. But, this is not fault. So, relay should not operate. SVM based algorithm differentiate this condition from that of internal fault condition and does not issue any relay command.

Fig. 6 shows the correct operation of relay during energization.





#### F. Over-Fluxing Condition

Under normal operating condition, transformer develops a maximum flux density in the core which is directly proportional to magnitude of applied voltage and is inversely proportional to frequency. Any deviation of these quantities from its nominal magnitudes, transformer core is subjected to over-fluxing condition but should not operate.

Fig. 7 the correct operation of relay even when operating current is majority of times greater than restrain current (when supply voltage is increased by 25% and frequency is reduced by 5%).

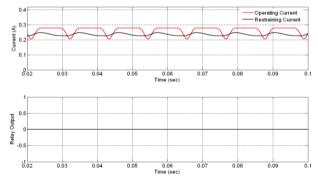


Fig 7: Relay output for over-fluxing Condition with operating and restrain current

#### **VII. CONCLUSION**

The foresaid method of SVM is successfully implemented on single phase transformer on simulation platform. The mal-functioning of conventional harmonic restrain method has been rectified using combination discrete wavelet transform and SVM. The all possible operating conditions are passed through powerful wavelet transformation to obtain the data in form of coefficients. These statistical data then used for training and testing purpose of SVM.

In this paper, Authors have used 30% data for training purpose and 70% data for testing purpose and found the efficiency of proposed algorithm to be 92.89%. This efficiency can be further improved by using majority of collected data for training purpose and remaining data for testing purpose but, that will slow down the operation of the system resulting in delayed decision making.



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Appendix

1)	Winding resistances	R1= 0.027603946 p.u. R2= 0.027603946 p.u.
2)	Winding inductances	L1= 0.022495099/314.5 p.u. L2=0.022495099/314.5 p.u.
3)	Magnetizing resistance	Rm= 500 p.u.
4)	Magnetizing inductance	Lm=80 p.u.

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